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January 2003 ACM SIGCHI Bulletin - a supplement to interactions, Volume 2003

Full text available: pdf(135.44 KB) Additional Information: full citation, index terms

Teaching future verification engineers: the forgotten side of logic design Fusun Ozguner, Duane Marhefka, Joanne DeGroat, Bruce Wile, Jennifer Stofer, Lyle Hanrahan June 2001 Proceedings of the 38th conference on Design automation



Full text available: pdf(115.08 KB) Additional Information: full citation, abstract, references, index terms

This paper describes a senior/graduate level course in hardware logic verification being offered by The Ohio State University in cooperation with IBM. The need for the course is established through the growing importance of logic verification to users of custom logic designs. We discuss the short-term and long-term goals for the course, and describe the course content and format. The course relies heavily on lab projects to illustrate the main concepts. Three projects and a final project re ...

3 Software process modelling: A precedence-based approach for proactive control in software process modelling



Josep M. Ribó, Xavier Franch

July 2002 Proceedings of the 14th international conference on Software engineering and knowledge engineering

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We present the proactive behavioural features of PROMENADE, a process modelling language for formalizing the construction of software process models. PROMENADE aims at improving expressiveness, standardization, flexibility and reuse in software process modelling. In this article we focus on expressiveness, which is achieved by means of a declarative (instead of imperative) proactive control-flow based on precedence relationships. Different families of such precedences have been defined within th ...

Keywords: UML, proactive control, software process models

Mining features for sequence classification Neal Lesh, Mohammed J. Zaki, Mitsunori Ogihara



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|   | microprocessor   |
|   | len-Tien Yen. Oichao Richard Yin   |

June 2000 Proceedings of the 37th conference on Design automation

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Multiprocessing (MP) design verification has been one of the bottlenecks for high performance microprocessor design projects. The problem is getting worse as the design complexity increases and more cache structures are integrated into one single chip. The challenges that MP verification faces today include: huge chip/system simulation model sizes, long simulation cycles, relative inefficiency of the simulation tools compared to uniprocessor, and so on. To solve these challenging problems, ...

6 Quality engineering: How one product team met the quality challenge Mark Changfoot, Ted Fines, Rod Lawson, Nick Lecic, Pat Morenz, Doug Mortson, Pramod Patel, Shelagh Roberts, Sheik Sahib, Rollie Sing

October 1993 Proceedings of the 1993 conference of the Centre for Advanced Studies on Collaborative research: software engineering - Volume 1

Full text available: pdf(1.63 MB) Additional Information: full citation, abstract, references

A primary goal of product development teams today is to ensure that quality is integral to everything they do. This article describes the initiatives the members of one product development team undertook to incorporate quality into every phase of their development process. The product was new, the team was new, and there was a strong commitment from every member of the team to prove just how much one product team could do to ensure the quality of its product. Development methodologies, process ma ...

# An approach to document processing

Robert Katz

April 1983 Proceedings of the 2nd annual international conference on Systems documentation

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Testing large software with automated software evaluation systems C. V. Ramamoorthy, S. F. Ho

April 1975 ACM SIGPLAN Notices, Proceedings of the international conference on Reliable software, Volume 10 Issue 6

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(1.19 MB) terms

In the past few years, research has been actively carried out in an attempt to improve the quality and reliability of large scale software systems. Although progress has been made on the formal proof of program correctness, proving large scale software systems correct by formal proof is still many years away. Automated software tools have been found to be valuable in improving software reliability and attacking the high cost of software systems. This paper attempts to describe some main fea ...

Keywords: Automated tools, Correctness, Performance, Software evaluation systems,

Software reliability

graduate programs

Jay F. Nunamaker, J. Daniel Couger, Gordon B. Davis

| 9  | A real world conversion to SGML  Dee Stribling, Tim Hunter, Len Olszewski, Anne Corrigan, Randy Mullis, Lloyd Allen  October 1996 Proceedings of the 14th annual international conference on Systems documentation: Marshaling new technological forces: building a corporate, academic, and user-oriented triangle  |  |
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|    | The primary objective of this paper is to analyze and evaluate the usefulness of object-oriented development and the Ada 95 programming language as applied to a specific software development project. A secondary objective is to show that structured development is still useful while applying object-oriented development and that the two methods can be integrated. The project is to develop an automated tool for generation of challenge examinations to test the knowledge of students in a given subjec |  |
| 11 | Automated aids fob reliable software  Donald J. Reifer   |  |
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|    | Recent investigations on the use of automation to realize the twin objectives of cost reduction and reliability improvement for computer programs developed for the U. S. Air Force are reported. The concepts of reliability and automation as they pertain to software are explained. Then, over twenty automated tools and techniques (aids) identified by this investigation are described and categorized. Based on the information reviewed, an assessment of the state of the technology is made. Finall      |  |
|    | <b>Keywords</b> : Automation, Programming tools, Software reliability, programming techniques  |  |

November 1982 **Communications of the ACM**, Volume 25 Issue 11

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| Accreditation standards have required computer science departments to teach software engineering for over a decade now. Industry statistics, however, suggest that good software engineering techniques often are not being carried out in large-scale software development projects, resulting in unusable software systems on which millions of dollars have been squandered. Society cannot afford to continue to squander precious resources on software fiascoes. We suggest that perhaps how we teach software |        |  |  |
| 15 Abstraction based activers development  | $\Box$ |  |  |
| 15 <u>Abstraction-based software development</u> Valdis Berzins, Michael Gray, David Naumann   |        |  |  |
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| Pair programming and pair trading: effects on learning and motivation in a CS2 course Timothy H. DeClue May 2003 The Journal of Computing in Small Colleges, Volume 18 Issue 5   |        |  |  |
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www.scionlabs.com/pg-art-removing-the-human-element.htm - 63k - Cached

#### 12. Test Case Catalog (TCC) Overview 旦

... Creating a **Test Plan** ... catalog during their LENS (CLECs Only), **TAG** application and/or validity, and EDI ... **T** descriptions of each ...

www.interconnection.bellsouth.com/clectest/tcc\_overview.html - 22k - Cached

#### 

... Function/Integration Test Plan. Client: ... to the Phone field XML tag the following: WEB BOOKING and the ... and expected results. ...

www.cs.sfu.ca/fas-info/cs/CC/275/fvandenb/2002/assignments/Function-Integration%20Test%20Plan%20Samp.

# 

... Issue Number: 10. Column Tag: Quality Control ... Just as soon as the test script is written changes in the ... A which defines ...

www.mactech.com/articles/mactech/Vol.13/13.10/SoftwareTestAutomation - 51k - Cached

### 

... Building a Web Test Plan. Building an Advanced Web Test Plan ... comma, so the csv tag is a misnomer in th listeners, a ...

jakarta.apache.org/jmeter/usermanual/component\_reference.html - 181k - Cached

#### 

... 2 and a new tag to test case DTD's (whatamess ... be used as basis for Test Plan and User Documentation ... on some ...

www.soberit.hut.fi/tik-76.115/00-01/palautukset/groups/Whatamess/t4/wm\_fs.html - 116k - Cached

#### 17. John R 旦

... Software Quality Automated **Tag** Tester. Acceptance testing development and ... testing. Developed **Test Plan script** creation environment using MS ...

johnr.benavides.com/ - 29k - Cached

# 18. <a href="http://www.fts.gsa.gov/smartcard\_b/conformance\_tests/Test\_Plan\_for\_Testing\_Cards.doc">http://www.fts.gsa.gov/smartcard\_b/conformance\_tests/Test\_Plan\_for\_Testing\_Cards.doc</a> (MIC ... Test Plan for GSC-IS Compliance Testing ... under the Capability Tuples (CT) tag of the CCC should conform program contains ...

www.fts.gsa.gov/smartcard\_b/conformance\_tests/Test\_Plan\_for\_Testing\_Cards.doc - 45k - View as html

# 

... SQA, Test strategy, **Test plan**, Test environment, Execution ... Developed **test plan** based on IEEE standard . and regression **test script** ...

www.portnov.com/../../keyboard/windowsdeveloper.htm - 128k - Cached

## 

... Recorder Module **Test Plan**. Author: Jukka Pirinen ... least one step of the **test-script** is failed then the test ... m "/whatamess:session"-**tag**. ...

www.soberit.hut.fi/tik-76.115/00-01/palautukset/groups/Whatamess/t2/wm\_tp\_recorder.html - 23k - Cached

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